

CLAIMS

What is claimed is:

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1. A method for matching output impedance of a driver to a load impedance,
2 comprising:
4 attaching an external impedance between an external contact and a first
6 source potential, wherein the load impedance comprises the external
8 impedance plus impedance of interconnections between an output
10 terminal of the driver and the external impedance;
12 attaching an adjustable impedance between a second source potential and
14 the output terminal of the driver;
16 obtaining a reference potential, wherein the reference potential has a value
18 half-way between the first source potential and the second source
potential; and
20 obtaining a load matching impedance by changing the adjustable
22 impedance until the absolute value of the difference between voltage of
the output terminal of the driver and the reference potential is less than a
preselected value.
2. The method as recited in claim 1, wherein the adjustable impedance
2 comprises a plurality of field effect transistors, wherein the field effect
transistors have capability of being individually turned on and turned off.
3. The method as recited in claim 1, providing the first source potential is
2 ground potential and the second source potential is a supply potential.

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- 2 4. The method as recited in claim 1, providing the first source potential is a supply potential and the second source potential is ground potential.
- 2 5. The method as recited in claim 1, wherein the method step of obtaining the reference potential comprises the method steps of :
- 4 attaching one end of each of two matched resistors together;
- 6 attaching the other end of one of the matched resistors to the first source potential;
- 8 attaching the other end of the other matched resistor to the second source potential; and
- 10 obtaining the reference potential from the point of attachment of the two matched resistors.
- 12 6. The method as recited in claim 1, wherein the method step of obtaining the adjustable impedance comprises:
- 2 turning on and off field effect transistors until the absolute value of the difference between the potential of the output terminal of the driver and the reference potential is less than a preselected value.
- 4 7. The method as recited in claim 1, further comprising the method steps of:
- 2 repeating the method obtaining the load matching impedance for a preselected number of conducting traces, wherein the conducting traces have different length to width ratios;
- 4

6 based on the conducting trace length to width ratio of an additional driver,
8 selecting the load matching impedance which provides the closest match
of the output impedance to the load impedance for the additional driver;
and

10 transferring an instruction to the additional driver to set the load matching
12 impedance of the additional driver to the result of the method step of
selecting the load matching impedance.

8. The method as recited in claim 7, wherein the method step of obtaining
2 the adjustable impedance is performed via a compensation circuit,
4 wherein the compensation circuit is capable of performing the method
step of obtaining the adjustable impedance for the preselected conducting
traces.

9. The method as recited in claim 7, wherein the adjustable impedance for
2 at least one of the drivers comprises a plurality of field effect transistors,
4 wherein the field effect transistors have capability of being individually
turned on and turned off.

10. The method as recited in claim 1, further comprising the method steps of:
2
4 based on the conducting trace length to width ratio of the driver and on
the conducting trace length to width ratio of an additional driver,
6 computing the load matching impedance which provides the closest
match of the output impedance to the load impedance for the additional
driver; and
8
10 transferring an instruction to the additional driver to set the load matching
impedance of the additional driver to the result of the method step of

computing the load matching impedance.

11. The method as recited in claim 10, wherein the method step of obtaining the adjustable impedance is performed via a compensation circuit, wherein the compensation circuit is capable of performing the method step of obtaining the adjustable impedance for the preselected conducting traces.

12. The method as recited in claim 10, wherein the adjustable impedance for at least one of the drivers comprises a plurality of field effect transistors, wherein the field effect transistors have capability of being individually turned on and turned off.

13. An electronic circuit for matching output impedance of a driver to a load impedance, comprising:

an external impedance attached between an external contact and a first source potential, wherein the load impedance comprises the external impedance plus impedance of interconnections between an output terminal of the driver and the external impedance;

an adjustable impedance attached between a second source potential and the output terminal of the driver; and

a reference potential source, wherein the reference potential obtained from the reference potential source has a value substantially half-way between the first source potential and the second source potential and wherein a load matching impedance is obtainable by changing the adjustable impedance until the absolute value of the difference between voltage of the output terminal of the driver and the reference potential is

18 less than a preselected value.

2 14. The electronic circuit as recited in claim 13, wherein the adjustable
4 impedance comprises a plurality of field effect transistors, wherein the
field effect transistors have capability of being individually turned on and
turned off.

2 15. The electronic circuit as recited in claim 13, wherein the first source
potential is ground potential and the second source potential is a supply
potential.

2 16. The electronic circuit as recited in claim 13, wherein the first source
potential is a supply potential and the second source potential is ground
potential.

2 17. The electronic circuit as recited in claim 13, wherein the reference
potential source comprises :

4 two matched resistors, wherein one end from one of the matched resistors
is connected to one end of the other matched resistor, the other end of one
6 of the matched resistors is connected to the first source potential, the other
end of the other matched resistor is connected to the second source
8 potential, and the reference potential is obtained at the point of attachment
of the two matched resistors.

2 18. The electronic circuit as recited in claim 13, further comprising:
4 a compensation circuit, wherein the compensation circuit comprises a
comparator having a first input, a second input, and an output, wherein
the compensation circuit comprises a control circuit, wherein the first

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6 input of the comparator is connected to the output terminal of the driver
and the second input of the comparator is connected to the reference
8 potential source, wherein the output of the comparator is attached to an
input of the control circuit, wherein, for a plurality of conducting traces
10 having different length to width ratios, the control circuit is capable of
changing the adjustable impedance until the absolute value of the
12 difference between the potential of the output terminal of the driver and
the reference potential is less than a preselected value, wherein the
14 compensation circuit is connected to at least one additional driver,
wherein the compensation circuit is capable of selecting the adjustable
16 impedance which provides the closest match of the output impedance of
the additional driver to the load impedance for the additional driver based
18 on the conducting trace length to width ratio of the additional driver, and
wherein the compensation circuit is capable of instructing the additional
20 driver to set the load matching impedance of the additional driver to the
value of the selected adjustable impedance.

19. The electronic circuit as recited in claim 13, further comprising:

2 a compensation circuit, wherein the compensation circuit comprises a
4 comparator having a first input, a second input, and an output, wherein
the compensation circuit comprises a control circuit, wherein the first
6 input of the comparator is connected to the output terminal of the driver
and the second input of the comparator is connected to the reference
8 potential source, wherein the output of the comparator is attached to an
input of the control circuit, wherein, for a conducting trace having a
10 known length to width ratio, the control circuit is capable of changing the
adjustable impedance until the absolute value of the difference between
12 the potential of the output terminal of the driver and the reference
potential is less than a preselected value, wherein the compensation

14 circuit is connected to at least one additional driver, wherein the
compensation circuit is capable of computing the adjustable impedance
16 which provides a match of the output impedance of the additional driver
to the load impedance for the additional driver based on the conducting
18 trace length to width ratio of the additional driver, and wherein the
compensation circuit is capable of instructing the additional driver to set
20 the load matching impedance of the additional driver to the value of the
selected adjustable impedance.

20. The electronic circuit as recited in claim 13, further comprising:

2 an open circuit detection circuit, wherein the open circuit detection circuit
4 is capable of detecting an open circuit at the external contact; and
6 a control circuit, wherein the control circuit receives input from the open
circuit detection circuit and wherein, when the external contact is an open
8 circuit, the control circuit is capable of changing the adjustable impedance
to a preselected value.

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